

First Named Inventor	Frankie F. Roohparvar	<p style="text-align: center;"><b>INFORMATION DISCLOSURE STATEMENT</b></p>
Serial No.	Unknown	
Filing Date	Filed herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Attorney Docket No.	400.021US02	
<p>Title: TOP/BOTTOM SYMMETRICAL PROTECTION SCHEME FOR FLASH</p>		

Mail Stop: Patent Application  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

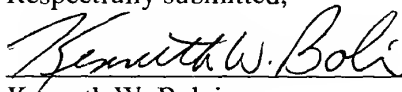
In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Applicant respectfully requests that this Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

Further pursuant to MPEP §609, because all of the references listed on the attached Form 1449 have been previously submitted and made of record in the parent application, U.S. Patent Application 09/608,256, filed June 30, 2000, copies of the references previously made of record in the parent application are not submitted herewith.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at the number listed below.

Date: 9/24/03

Respectfully submitted,

  
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T – 612/312-2200  
F – 612/312-2250

Applicant(s)	Roohparvar, et al.	<b>INFORMATION DISCLOSURE STATEMENT FORM PTO-1449</b>
Serial No.	09/608,256	
Filing Date	June 30, 2000	
Group Art Unit	2751	
Examiner Name	Unknown	
Attorney Docket No.	400.021US01	
Title: TOP/BOTTOM SYMMETRICAL PROTECTION SCHEME FOR FLASH		Sheet 1 of 1

U.S. Patent References				
Examiner Initials	Patent No.	Issue Date	Name	Filing Date
	5,041,886	08/20/1991	Lee	09/25/89
	5,526,364	06/11/1996	Roohparvar	02/10/1995
	5,537,354	07/16/1996	Mochizuki et al.	12/14/1994
	5,432,927	07/11/1995	Grote et al.	11/17/1994
	5,539,894	07/23/1996	Webber	04/20/1993
	5,546,561	08/13/1996	Kynett et al.	01/13/1995
	5,566,323	10/15/1996	Ugon	10/24/1994
	5,598,548	01/28/1997	Matsuo et al.	11/03/1994
	5,600,605	02/04/1997	Schaefer	06/07/1995
	5,666,321	09/09/1997	Schaefer	09/01/1995
	5,673,222	09/30/1997	Fukumoto et al	06/20/1996
	5,701,492	12/23/1997	Wadsworth et al.	03/29/1996
	5,751,039	05/12/1998	Kauffman et al.	09/19/1997
	5,787,457	07/28/1998	Miller et al.	10/18/1996
	5,936,903	08/10/1999	Jeng et al.	08/07/1998
	5,943,263	08/24/1999	Roohparvar	01/08/1997
	5,978,273	11/02/1999	Shigemura	03/30/1998
	5,995,438	11/30/1999	Jeng, et al.	08/07/1998
	6,026,016	02/15/2000	Gafken	05/11/1998
	6,026,465	02/15/2000	Mills et al.	06/18/1997
	6,028,798	02/22/2000	Roohparvar	03/11/1997
	6,031,757	02/29/2000	Chuang et al	11/22/1996
	6,034,889	03/07/2000	Mani et al	10/23/1998
	6,137,133	10/24/2000	Kauffman et al.	05/11/1998
	6,141,247	10/31/2000	Roohparvar et al.	12/21/1998
	6,154,819	11/28/2000	Larsen et al.	05/11/1998
	6,490,202	12/03/2002	Roohparvar	04/06/2001

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	T?
	Country	No.			
	EP	EP 0 806 772 A2	Motorola, Inc.	12.11.97	

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
	Keeth, et al., "DRAM circuit design: a tutorial," IEEE Press, 2001, pp.16-23, 142-153
	Micron Semiconductor Products, Inc., "2Mb, Smart 5 BIOS-Optimized Boot Block Flash Memory," <u>Flash Memory</u> www.micron.com, copyright 2000, Micron Technology, Inc., pp. 1-12
	Micron, "16 Mb: x16 SDRAM" <u>Synchronous DRAM</u> , www.micron.com, copyright 1999 Micron Technology, Inc., pp. 1-51

Examiner Signature		Date Considered	
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			